

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/718,420	CORCORAN ET AL.	
Examiner		Art Unit	2166	Page 1 of 1
Srirama Channavajjala				

U.S. PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
A	US-			
B	US-			
C	US-			
D	US-			
E	US-			
F	US-			
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I	US-			
J	US-			
K	US-			
L	US-			
M	US-			

FOREIGN PATENT DOCUMENTS

*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
N	WO 02/29624	04-2002	PCT	Huetter,Raymond	17/30
O					
P					
Q					
R					
S					
T					

NON-PATENT DOCUMENTS

*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)		
	U	Eero Lassila, "A macro expansion approach to embedded processor code generation", 1996	
	V		
	W		
	X		

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.